

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : YAMAZAKI et al. Art Unit : Unknown  
Serial No. : Not yet assigned Examiner : Unknown  
Filed : January 26, 2001  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 26, 2001

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Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-008001	Application No. Not yet assigned
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))		Applicant YAMAZAKI et al.		
		Filing Date January 26, 2001	Group Art Unit Unknown	

<b>U.S. Patent Documents</b>							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,643,826	July 1, 1997	Ohtani et al.			
	AB	5,923,962	July 13, 1999	Ohtani et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

PRO  
 01/26/01  
 U.S. 769165  
 C983 09/60

<b>Foreign Patent Documents or Published Foreign Patent Applications</b>							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes      No
	AL						
	AM						
	AN						
	AO						
	AP						

<b>Other Documents (include Author, Title, Date, and Place of Publication)</b>		
Examiner Initial	Desig. ID	Document
	AQ	Japanese Patent Application Laid-Open No. 10-048640A (English Abstract attached)
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	